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Patent

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Ajanovic, et al.

Examiner: Lefkowitz, Sumati

Application No. 09/428,134

Art Unit: 2189

Filed: October 26, 1999

For: METHOD AND APPARATUS  
FOR AN IMPROVED INTERFACE  
BETWEEN A MEMORY CONTROL  
HUB AND AN INPUT/OUTPUT  
CONTROL HUB

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Date

John P. Ward

Commissioner For Patents  
Washington, D.C. 20231-0001

RESPONSE AND AMENDMENT

Sir:

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Technology Center 2100

Applicants respectfully request that the above-identified application be amended as follows:

IN THE CLAIMS:

Following are the claims as amended herein and as are currently pending for consideration:

1. An interface to transfer data directly between a memory control hub (MCH) and a input/output control hub (ICH) within a computer system, comprising:  
a data signal path to transmit data in packets via split transactions; and